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## United States Patent [19]

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[11]

## [54] PROCESS AND SYSTEM FOR FLATTENING SECONDARY EDGEBEADS ON RESIST COATED WAFERS

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#### Related U.S. Application Data

[62] Division of Ser. No. 306,920, Sep. 16, 1994, Pat. No. 5,750,317.

[52] **U.S. Cl.** ...... **430/327**; 430/311; 134/33; 427/240; 427/273

#### [56] References Cited

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Primary Examiner-Kathleen Duda

#### [57] ABSTRACT

A method and system of flattening resist mounds formed during a wet edgebead operation. The wet edgebead operation is used to remove edgebeads formed when a resist material is deposited on a semiconductor wafer. Solvent is introduced to the semiconductor wafer at the area containing the resist mounds to soften them, and the semiconductor wafer is spun at a high speed to flatten the mounds.

#### 13 Claims, 7 Drawing Sheets

