

United States Patent [19]

Linliu

[54]	PLANARIZATION PROCESS USING ARTIFICIAL GRAVITY						
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[56]		References Cited					

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ABSTRACT [57]

The time needed to planarize the surface of an integrated circuit is reduced by causing the planarization liquid to settle in the presence of artificial gravity that supplements natural gravity. A number of different ways to achieve artificial gravity are described. These include centrifuging, magnetic repulsion, vertical pulling by a motor, and providing a pressure differential between the top and bottom sides of the wafer holder.

9 Claims, 4 Drawing Sheets

